8-bit AVR® Microcontrollers Peripheral Integration

Quick Reference Guide

Product Family	Pin Count	Program Flash Memory (KB)	SRAM (KB)	Peripheral Function Focus: Intelligent Analog							Waveform Control				Timing and Measurements				Logic, Crypto and Math			Safety and Monitoring				Communications						User Interface			System Flexibility					
				ADC (# of bits)	ADC (# of channels)	Comparators	ADC Gain Stage	DAC (# of bits)	Temperature Sensor	Internal Voltage Reference	8-bit PWM	16-bit PWM	Quadrature Decoder	Waveform Extension	Real-Time Counter	8-bit Timer/Counters	12-bit Timer Counter	16-bit Timer/Counter	CCL	MULT	Crypto (AES/DES)	CRC	POR	BOD	WDT	UART	USART	USB	I^2C	SPI	IRCOM	QTouch® Technology	QTouch Technology with PTC[2]	LCD	External Bus Interface	DMA Channels	Event System	SleepWalking	Sleep Modes	picoPower® Technology
ATtiny4/5/9/10	6	0.5–1	0.032	10[3]	4[3]	✓						2						1					✓		✓							✓							4	
ATtiny102/104	8/14	1	0.032	10	5/8	✓				✓		2						2					✓		✓		1												4	
ATtiny13A	8–20	1	0.064	10	4	✓						2											✓	✓	✓							✓							3	✓
ATtiny20/40	12–20	2/4	0.128/0.256	10	8/12	✓			✓		2	2				1		1					✓	✓	✓				1	1		✓							4	
ATtiny24A/44A/84A	14–20	2–8	Up to 0.512	10	8	✓	✓		✓	✓	2	2				1		1		✓			✓	✓	✓				1	1		✓							4	✓
ATtiny25(V)/45(V)/85(V)	8–20	2–8	Up to 0.512	10	4	✓	✓		✓	✓	4					2				✓			✓	✓	✓				1	1		✓							3	
ATtiny48/88	28–32	4/8	Up to 0.512	10	8	✓			✓	✓	1	1				1		1					✓	✓	✓				1	1									3	✓
ATtiny87/167	20–32	8/16	0.512	10	11	✓			✓	✓	1	2				1		1		✓			✓	✓	✓	1[1]			1	2									4	
ATtiny261A/461A/861A	20–32	2–8	Up to 0.512	10	11	✓	✓		✓	✓						1		1					✓	✓	✓				1	1		✓							4	✓
ATtiny21x/41x/81x/161x/321x	8–24	2–32	Up to 2	10	12	✓		8	✓	✓		2			✓		1	1	✓	✓		✓	✓	✓	✓		1[1]		1	1			✓[4]				✓	✓	3	✓
ATtiny441/841	14–20	4/8	Up to 0.512	10	12	✓	✓		✓		1	2				1		2					✓	✓	✓		2		1	1									4	✓
ATtiny1634	20	16	1	10	12	✓			✓	✓	2	2				1		1		✓			✓	✓	✓		2		1				✓						4	✓
ATtiny2313A	20	2	0.128	–	–	✓				✓	2	2				1		1		✓			✓	✓	✓		1		1	2									3	✓
ATmega8A/16A/32A	28–44	8–32	1–2	10	8	✓					2	1			✓	2		1		✓			✓	✓	✓		1		1	1		✓							5	
ATmega8U2/16U2/32U2	32	8–32	0.5–1	–	–	✓			✓	✓	4	6			✓	2		3		✓			✓	✓	✓		2	✓	2	2			✓						6	
ATmega16U4/32U4	32	16/32	1/2	10	12	✓			✓	✓	5					1		1		✓			✓	✓	✓		1	✓		1									6	
ATmega48PB/88PB/168PB/328PB	32	4–32	0.5–2	10	8	✓			✓	✓	4	2/6[6]			✓	2		1/3[6]		✓			✓	✓	✓		1/2[6]		1/2[6]	1/2[6]		✓	1[6]						6	
ATmega64A/128A	64	64–128	4	10	8	✓	✓			✓	2	6				2		2		✓			✓	✓	✓		2		1	1		✓							6	
ATmega164PA/324PA/644PA/1284P	44	16–128	1–16	10	8	✓	✓			✓	4	2/2/4			✓	2		1/1/2		✓			✓	✓	✓		2		1	1		✓							6	✓
ATmega165PA/325PA/645P	44	16–64	1–4	10	8	✓				✓	4	6			✓	2		3		✓			✓	✓	✓		3		2	2			✓						6	✓
ATmega169PA/329PA/649P	64	16–64	1–4	10	8	✓				✓	2	2			✓	2		1		✓			✓	✓	✓		1		1	1		✓		✓					5	
ATmega324PB	44	32	2	10	8	✓				✓	2	2			✓	2		1		✓			✓	✓	✓		1		1	1		✓	✓						5	
ATmega640/1280/2560/1281/2561	64–100	64–256	8	10	8/16	✓	✓			✓	4	6/12			✓	2		4		✓			✓	✓	✓		2/4		1	1		✓			✓[5]				6	
ATmega3290PA/6490P	100	32–64	2–4	10	8	✓	✓			✓	2	2			✓	2		1		✓			✓	✓	✓		1		1	1		✓		✓					5	
ATmega3250PA/6450P	100	32–64	2–4	10	8	✓	✓			✓	2	2			✓	2		1		✓			✓	✓	✓		1		1	1		✓							5	
ATxmega A1U Family	100	64–128	4–8	12	16	✓	✓	12	✓	✓		8	✓	✓	✓			8		✓	✓	✓	✓	✓	✓		8	✓	4	4	✓	✓			✓	4	✓		5	✓
ATxmega A3U Family	64	64–256	4–16	12	16	✓	✓	12	✓	✓		7	✓	✓	✓			7		✓	✓	✓	✓	✓	✓		7	✓	2	3	✓	✓				4	✓		5	✓
ATxmega A4U Family	44–49	16–128	2–8	12	12	✓	✓	12	✓	✓		5	✓	✓	✓			5		✓	✓	✓	✓	✓	✓		5	✓	2	2	✓	✓				4	✓		5	✓
ATxmega B1/B3 Family	64–100	64–128	4–8	12	8	✓	✓		✓	✓		2/3	✓	✓	✓			2/3		✓	✓	✓	✓	✓	✓		1/2	✓	1	1	✓	✓		✓		2	✓		5	✓
ATxmega C3/D3 Family	64	32–384	4–32	12	16	✓	✓		✓	✓		5	✓	✓	✓			5		✓		✓	✓	✓	✓		3	✓[7]	2	2	✓	✓					✓		5	✓
ATxmega C4/D4 Family	44–49	16–128	2–8	12	12	✓	✓		✓	✓		4	✓	✓	✓			4		✓		✓	✓	✓	✓		2	✓[7]	2	2	✓	✓					✓		5	✓
ATxmega E5 Family	32	8–32	1–4	12	16	✓	✓	12	✓	✓		3	✓	✓	✓			3	✓	✓		✓	✓	✓	✓		2		1	1	✓	✓				4	✓		5	✓

1: LIN port also **2:** Peripheral Touch Controller **3:** Only on the ATtiny5/10 **4:** Not on the ATtiny212/214/412/414/416 **5:** Only on the ATmega1281/2561 **6:** Only on the ATmega328PB **7:** Only on the C3 and C4

Terminology

INTELLIGENT ANALOG: Sensor Interfacing and Signal Conditioning	
ADC: Analog-to-Digital Converter	General purpose 10-/12-bit ADC
ADC Gain Stage: Analog-to-Digital Converter Gain Stage	Programmable gain stage, providing amplification steps on the differential input voltage
Comp: Comparator	General purpose rail-to-rail comparator
DAC: Digital-to-Analog Converter	Programmable voltage reference with multiple internal and external connections
VREF: Voltage Reference	Stable fixed voltage reference for use with integrated analog peripherals
WAVEFORM CONTROL: PWM Drive and Waveform Generation	
PWM: Pulse Width Modulation	General purpose 10-bit PWM control
16-bit PWM: Standalone 16-bit PWM and 16-bit Timer/Counter	1. High-resolution 16-bit PWM with edge- and center-aligned modes 2. General purpose 16-bit timer/counter
TIMING AND MEASUREMENTS: Signal Measurement with Timing and Counter Control	
8-/12-/16-bit Timer	General purpose 8-/12-/16-bit timer/counter
LOGIC, CRYPTO AND MATH: Customizable Logic and Math Functions	
CCL: Configurable Custom Logic	1. Integrated combinational and sequential logic 2. Customer interconnection and re-routing of digital peripherals
MULT: Hardware Multiplier	MULTIPLY function of two 8-bit values with 16-bit result
Crypto (AES/DES)	Data encryption and decryption can be easily performed for both internally stored data or for small external data packets
SAFETY AND MONITORING: Hardware Monitoring and Fault Detection	
CRC/SCAN: Cyclical Redundancy Check with Memory Scan	Automatically calculates CRC checksum of Program/DataEE memory for NVM integrity

COMMUNICATIONS: General, Industrial, Lighting and Automotive	
USART: Universal Asynchronous Receiver Transceiver	1. General purpose serial communications 2. Support for LIN/IrDA®
I²C: Inter-Integrated Circuit	General purpose 2-wire serial communications
SPI: Serial Peripheral Interface	General purpose 4-wire serial communications
IRCOM: Infrared Communication Module	Encodes and decodes data according to the IrDA communication protocol
USER INTERFACE: Capacitive Touch Sensing and LCD Control	
LCD: Liquid Crystal Display	Highly integrated segmented LCD controller
QTouch®: Microchip Proprietary Touch Technology	Provides a simple-to-use solution to realize touch-sensitive interfaces
QTouch with PTC: QTouch with Peripheral Touch Controller	Provides a simple-to-use solution to realize touch-sensitive interfaces with a Peripheral Touch Controller
LOW POWER AND SYSTEM FLEXIBILITY: Low-Power Technology, Peripheral and Interconnects	
DMA: Direct Memory Access	Moves data between memories and peripherals without CPU overhead, improving overall system performance and efficiency
Event System	Flexible routing of peripheral events, ability to control peripheral independent from the CPU
External Bus Interface	Highly flexible module for interfacing external memories and memory-addressable peripherals
picoPower® Technology	Low-power technology
Sleep Modes	Low-power saving modes, IDLE, power-down, power-save, standby and extended standby
SleepWalking	Ability to put the CPU core to sleep until a relevant event occurs

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